

# FIRST ANNOUNCEMENT AND CALL FOR PAPERS

## 2024 IEEE SYMPOSIUM ON VLSI TECHNOLOGY & CIRCUITS

BRIDGING THE DIGITAL AND PHYSICAL WORLDS  
with efficiency and intelligence

Sunday, June 16 – Thursday, June 20, 2024

Hilton Hawaiian Village, Honolulu, Hawaii USA

### Paper Submission Deadline

Monday, February 5, 2024 (23:59 PST)

[www.vlssymposium.org](http://www.vlssymposium.org)

### Scope

The Symposium calls for papers in the following areas:

- Advanced CMOS Platforms, Interconnect and Backside Power Delivery Network (BSPDN) Technologies
- Advanced Packaging, Chiplet and Heterogeneous Integration Technologies Including 2.5D and 3D
- Analog and Mixed-Signal Circuits
- Beyond CMOS Devices That Utilize New Physics Including Spin, Optical and Quantum Computing
- Biomedical Devices, Circuits, and Systems
- Data Converters
- Device Physics, Characterization, Modeling and Reliability
- Devices and Accelerators For ML/DL and New Compute
- Digital Circuits, Hardware Security, Signal Integrity, IOs
- DTCO and Design Enablement
- Frequency Generation and Clocking Circuits
- Memory Technologies, Devices, Circuits, and Architectures
- Power Management Devices and Circuits
- Processes and Materials for CMOS Scaling and New Devices
- Processors and SoCs
- Sensors, Imagers, IoT, MEMS, Display Circuits
- Wireless and RF Devices Circuits and Systems
- Wireline and Optical Transceivers, Optical Interconnects and Processors

### Call for Workshops

The Symposium provides valuable opportunities for volunteers to apply to organize and host short workshops at the Symposium. **The call for workshop proposals and details will be announced soon.**

### Highlights

The Symposium will be a fully in-person event with live sessions at the Hilton Hawaiian Village to foster networking, with on-demand access to technical sessions available one week following the Symposium. The 5-day event will include:

- Plenary Sessions
- Technical Sessions
- Evening Panels
- Short Courses
- Workshops
- Demo Session for outstanding papers
- SSCS/EDS Women in Engineering and Young Professionals events
- Hawaiian Luau celebration

### Paper Submission

Prospective authors are invited to submit two-page extended abstracts to [www.vlssymposium.org](http://www.vlssymposium.org). Accepted papers will be published as submitted, with no revisions permitted. Authors must follow detailed instructions provided within the "Authors" section of the website, including the Authors' Guide and Pre-publication Policy.

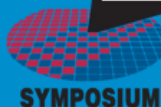
**Students are encouraged to apply to the prestigious Best Student Paper award.**

**Extended versions of outstanding papers will be invited for publication in the IEEE Transaction on Electron Devices, IEEE Journal of Solid-State Circuits, and IEEE Solid-State Circuits Letters.**

### Further Information

The Symposium website – [www.vlssymposium.org](http://www.vlssymposium.org) – is the central resource for information, including details on paper submissions and Best Student Paper eligibility.

2024 VLSI



#### Symposium Chairs

Gosia Jurczak, Lam Research  
Borivoje Nikolić, Univ. of California, Berkeley

#### Symposium Chairs

Takaaki Tsunomura, Tokyo Electron Limited  
Mototsugu Hamada, The University of Tokyo

#### Program Chairs

Vijay Narayanan, IBM T. J. Watson Research Center  
Ron Kapusta, Analog Devices

#### Program Co-Chairs

Kazuhiko Endo, Tohoku University  
Sugako Otani, Renesas Electronics Corporation

#### Secretariat (North America and Europe)

Canfield Event Management, LLC  
+1 972-637-6641, [vlssymposium@ieee.org](mailto:vlssymposium@ieee.org)

#### Secretariat (Japan and Far East)

JTB Communication Design, Inc., Tokyo, Japan  
+81-3-5657-0777, [vlssymp@jtbcom.co.jp](mailto:vlssymp@jtbcom.co.jp)

